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## A deep level transient spectroscopic study of boron-ion-implanted $\text{Si}_{1-x}\text{Ge}_x/\text{Si}$ single quantum wells

Fang Lu, Jianbao Wang, Jiayu Jiang, Dawei Gong, Henghui Sun and Xun Wang

Surface Physics Laboratory and Fudan T D Lee Physics Laboratory, Fudan University, Shanghai, 200433, People's Republic of China

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**Abstract.** The defects induced by boron-ion implantation with a relatively low dosage of  $1 \times 10^{12} \text{ cm}^{-2}$  in  $\text{Si}_{1-x}\text{Ge}_x/\text{Si}$  single quantum wells are studied by deep level transient spectroscopy (DLTS). For low Ge content  $x$ , a defect level  $\text{H}_2$  at an energy of 0.52 eV above the silicon valence band edge was found in the well region and its boundaries. For samples with higher Ge content, such that the strain is released, an electron trap  $\text{E}_2$  rather than  $\text{H}_2$  is formed by the ion implantation. Rapid thermal annealing at 600 °C removes most of the  $\text{H}_2$  defects induced by the ion implantation without changing the properties of the quantum well.

Ion implantation is a standard semiconductor device technology and is likely to be beneficially employed in fabricating  $\text{Si}_{1-x}\text{Ge}_x/\text{Si}$  heterostructure devices. In order to benefit from this technique the effects of ion implantation and post-implantation thermal annealing on the crystalline structures should be understood. In previous work various structural characterization techniques, such as Rutherford backscattering spectrometry, transmission electron microscopy and x-ray double-crystal diffractometry, have been used to study the damage and strain in epitaxial  $\text{Si}_{1-x}\text{Ge}_x$  alloy layers induced by ion implantation and the relationship of stress to the crystalline regrowth process during the post-implantation thermal annealing treatment [1–5]. However, for  $\text{Si}_{1-x}\text{Ge}_x/\text{Si}$  quantum wells, work on studying the creation and elimination of defects induced by the ion implantation and post-implantation thermal annealing has not been reported yet. In this work, we have investigated defects in boron-ion-implanted  $\text{Si}_{1-x}\text{Ge}_x/\text{Si}$  single quantum well samples by using deep level transient spectroscopy (DLTS). The results show that the defects created by boron-ion implantation are related to the built-in strain in the quantum well structure. Rapid thermal annealing (RTA) at 1000 °C after the ion implantation fully removed the residual defects.

The samples used in this study were grown by molecular beam epitaxy at a temperature of 500 °C on p-type Si(100) substrates with a hole concentration of  $2 \times 10^{18} \text{ cm}^{-3}$ . The  $\text{Si}_{1-x}\text{Ge}_x$  well layer is sandwiched between a Si buffer layer 300–500 nm thick and a Si cap layer of thickness 300 nm. The structural parameters for different quantum well samples are listed in table 1. The unintentionally doped Si buffer, Si cap and  $\text{Si}_{1-x}\text{Ge}_x$  epilayers were found to be p-type with a doping concentration of about  $1 \times 10^{16} \text{ cm}^{-3}$ . The boron-ion implantation was performed at an energy of 40 keV and a dosage of  $1 \times 10^{12} \text{ cm}^{-2}$ . From the computer simulation the peak boron concentration is estimated to be  $9 \times 10^{16} \text{ cm}^{-3}$  and is located near the centre of the cap layer, the full width at half maximum of the

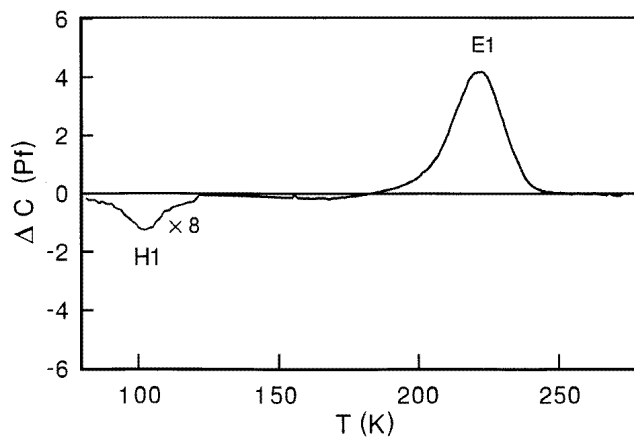
boron distribution being about 45 nm. So the implanted ions are basically constrained to be within the cap layer and do not reach the quantum well region. Because of the low dose, it was found [1] that the cap layer still remains in the crystalline form after the implantation. Thus it is possible to measure DLTS signals after the implantation without post-implantation annealing. The sample was fabricated into a testing structure. An ohmic contact was made on the back side by evaporating Al followed by alloying in  $N_2$  ambient at  $500^\circ\text{C}$  and a Schottky contact was formed on the top of the cap layer by evaporating Al through a mask without alloying. The diameter of the Schottky electrode is about 0.8 mm.

**Table 1.** Structural parameters of quantum well samples.

	Samples			
	S <sub>1</sub>	S <sub>2</sub>	S <sub>3</sub>	S <sub>4</sub>
Well width $L_w$ (nm)	15	15	15	3
Ge composition ( $x$ )	0.33	0.40	0.53	0.33
$h_c^i$ (nm) <sup>a</sup>	5.5	4.5	2.7	5.5
$h_c^e$ (nm) <sup>b</sup>	30	20	7	30

<sup>a</sup> According to the theory of Matthews and Blackeslee [8].

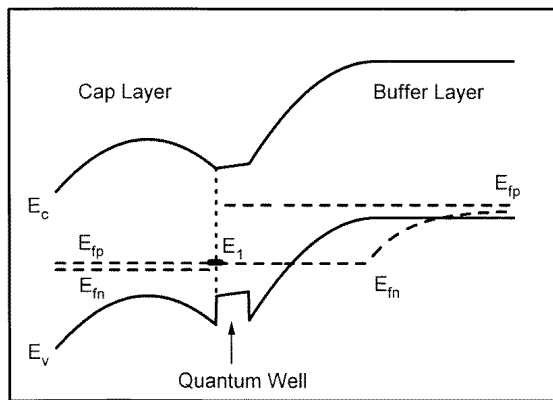
<sup>b</sup> The critical thickness according to Bean *et al* [9].



**Figure 1.** The DLTS spectrum of unimplanted sample S<sub>1</sub>; measured at  $V_R = -2$  V and  $V_p = 3$  V.

The DLTS spectrum of unimplanted sample S<sub>1</sub> measured at a reverse bias  $V_R$  of  $-2$  V and with an injection pulse amplitude  $V_p$  of 3 V is shown in figure 1 in which two peaks labelled H<sub>1</sub> and E<sub>1</sub> can be clearly seen. Peak H<sub>1</sub> is a majority carrier peak which originates from the hole emission from the well to the barrier region [6], since the quantum well could be seen as a ‘big trap’ which acts just like a deep level trap in terms of carrier emission and capture. According to the thermal emission model of Debbar *et al* [7], the activation energy of peak H<sub>1</sub> which is deemed as the energy difference between the hole ground state in the well and the Si barrier is determined from the DLTS to be 0.23 eV which is very close to the predicted valence band offset of 0.24 eV. It is reasonable since the well width of the sample S<sub>1</sub> is 15 nm so the hole ground state is almost equal to the valence band

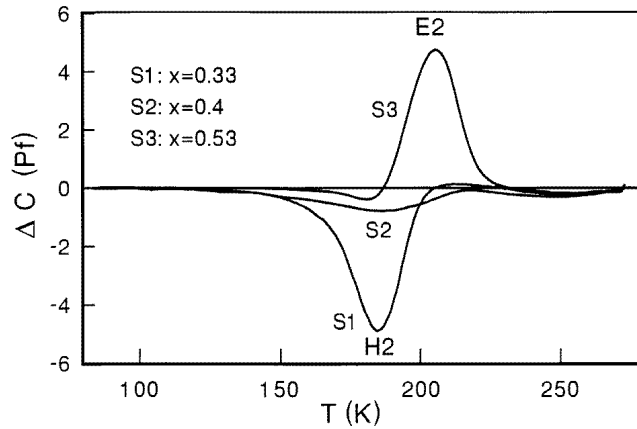
edge of the  $\text{Si}_{1-x}\text{Ge}_x$  well. This coincidence verifies that the assignment of  $\text{H}_1$  to the well emission is correct. The existence of peak  $\text{H}_1$  is indicative of the good crystalline quality of the sample, otherwise the carrier emission from the well would have been smeared out by the emission from other defects or by the leakage current. Peak  $\text{E}_1$  is a minority carrier peak which is not observable in the ordinary DLTS measurement of a p-type Schottky diode structure. The origin of this minority carrier peak has been discussed in our previous work [6, 8]. Suppose that there exist donor-like defects at the interface of a p-type heterojunction, then the quasi-Fermi level of electrons will be pinned near the defect level if the defect density is relatively large. Figure 2 is the energy band diagram which shows the relative positions of the conduction band and valence band edges  $E_c$  and  $E_v$  the defect levels  $E_1$  and quasi-Fermi levels  $E_{Fn}$  and  $E_{Fp}$  under a reverse bias voltage. When the bias voltage changes a small variation in the Fermi level will lead to a great change in the electron population in the defect level and thus a reduction in the number of negative charges in the space-charge region. In order to maintain electrical neutralization, the space-charge region is thus expanded, leading to a decrease in capacitance and giving rise to a minority carrier peak in the DLTS measurement. Except for the interfacial defects the original sample  $\text{S}_1$  is free of other defects, as shown in figure 1.



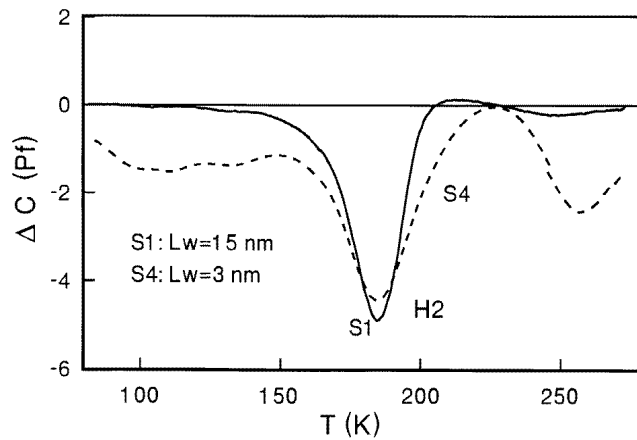
**Figure 2.** A schematic band diagram of a reverse-biased Schottky diode with a single quantum well in the depletion region.

The DLTS spectra of a series of implanted samples  $\text{S}_1$ ,  $\text{S}_2$ ,  $\text{S}_3$  and  $\text{S}_4$  are shown in figures 3 and 4. The well emission signal is remarkably reduced. Instead a new majority carrier peak  $\text{H}_2$ , attributed to a defect level induced by the ion implantation, was seen in DLTS. The energy level of defect  $\text{H}_2$  is determined to be  $E_v + 0.52$  eV, where  $E_v$  is the valence band edge of Si. The ratio of the peak height  $\Delta C$  to the steady state capacitance  $C$  in DLTS is proportional to the apparent defect density of  $\text{H}_2$ . From figure 3 it could be seen that the densities of defect  $\text{H}_2$  are different depending on the Ge composition in the well, but the peak temperatures, namely the energies of the defect levels are the same. On the other hand it can be seen from figure 4 that the defect density of  $\text{H}_2$  is almost independent of the well width if the Ge compositions in the wells are the same.

Before giving an analysis of the relation between the defect density of  $\text{H}_2$  and the well structure, let us first investigate the depth distribution of the defect  $\text{H}_2$ . By changing the bias voltage, the space-charge region of the Schottky barrier would extend over the cap and the quantum well. The DLTS spectra of sample  $\text{S}_1$  measured at different bias voltages are shown

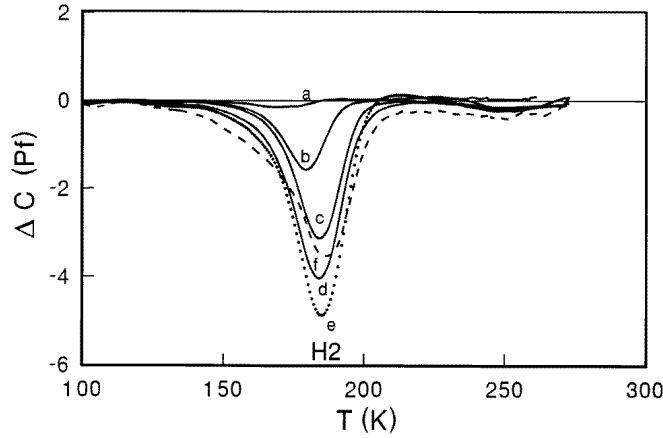


**Figure 3.** DLTS spectra of samples S<sub>1</sub>, S<sub>2</sub> and S<sub>3</sub> measured at  $V_R = -2$  V and  $V_P = 3$  V.



**Figure 4.** DLTS spectra of samples S<sub>1</sub> (full curve) and S<sub>4</sub>, (broken curve) measured at  $V_R = -2$  V and  $V_P = 3$  V.

in figure 5. From the capacitance–voltage measurements and the numerical simulation (not shown here), the boundary of the space-charge region at zero bias is expected near the well boundary. Due to the carrier accumulation in the quantum well the boundary of the space-charge region is basically pinned within the well region as the bias voltage varies from  $-0.5$  to  $-2.0$  V. At the bias voltage of  $-2.5$  V, the holes in the quantum well are depleted and the space-charge region is extended into the buffer layer. The DLTS peaks in figure 5 are thus related to the defects distributed basically in the well region. The monotonic increase in peak height with bias voltage from  $-0.5$  to  $-2.0$  V is consistent with the above assignment. In order to determine the defect distribution further, the surface of S<sub>1</sub> was etched by anodic oxidation and HF step by step after boron-ion implantation. Table 2 shows the experimental values of  $\Delta C/C$  after etching away different layer thicknesses. The defect density does not change even after more than half of the cap layer has been removed, but disappears fully in DLTS if the cap layer and the quantum well have been etched away completely. This means that there is no defect in the buffer and the substrate. This again supports the



**Figure 5.** The dependence of the majority carrier peak  $H_2$  of sample  $S_1$  on  $V_R$  and  $V_P$ : (a)  $V_R = 0$  V,  $V_P = 1$  V; (b)  $V_R = 0.5$  V,  $V_P = 1.5$  V; (c)  $V_R = -1.0$  V,  $V_P = 2.0$  V; (d)  $V_R = -1.5$  V,  $V_P = 2.5$  V; (e)  $V_R = -2.0$  V,  $V_P = 3.0$  V and (f)  $V_R = -2.5$  V,  $V_P = 3.5$  V.

**Table 2.**  $\Delta C/C$  of defect  $H_2$  after etching away different layer thicknesses.

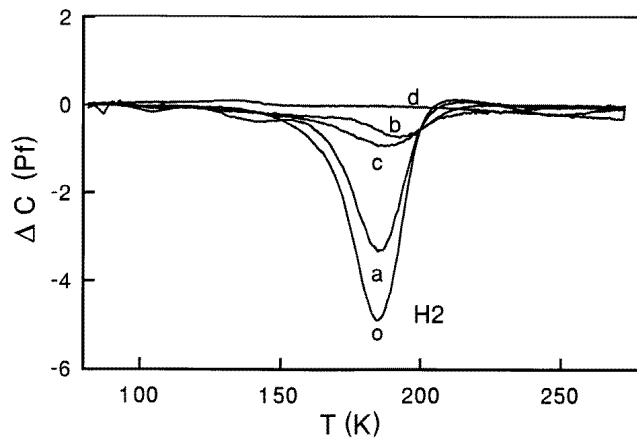
	Thickness removal (nm)			
	0	90	180	360
$\Delta C$ (pF)	4.89	7.43	2.77	0
$C$ (pF)	130	210	74	80
$\Delta C/C$	0.037	0.035	0.037	0

hypothesis that the measured defects induced by ion implantation are mostly distributed in the well region, including its boundary areas. It is not known whether there exists a defect in the cap layer which is included in the space-charge region of the Schottky barrier even at zero bias.

The defect  $H_2$  occurs in the quantum well region which the implanted ions do not reach, as described previously. The origin of the defect  $H_2$  in the unimplanted well region seems to be due to the interaction between the ion-implantation-induced damage and the misfit strain in the quantum well. The fact that the defect densities of  $H_2$  are different for samples with different Ge contents implies that the defect is related to the magnitude of the built-in strain. In samples  $S_1$ ,  $S_2$  and  $S_3$ , the misfit strains are different. The lattice mismatches of quantum wells are 0.014, 0.017 and 0.022 for samples  $S_1$ ,  $S_2$  and  $S_3$ , respectively; the critical thicknesses for pseudomorphic growth of these three samples are different. Listed in table 1 are the critical thicknesses  $h_c^t$  predicted theoretically by Matthews and Blakeslee based on the mechanical equilibrium model [9] and the experimental values  $h_c^e$  found by Bean *et al* [10]. For sample  $S_3$ , the layer thickness of the quantum well is larger than both  $h_c^e$  and  $h_c^t$ , therefore the strain is expected to be fully relaxed. The well thickness of sample  $S_1$  is well below  $h_c^e$ , so the strain is expected to be the largest among those of these three samples. For sample  $S_2$ , the well thickness is close to  $h_c^e$ , so it is suggested that the strain is partially relaxed. This argument is supported by our admittance spectroscopic measurements of the valence band offset  $\Delta E_v$ . The measured  $\Delta E_v$  of sample  $S_1$  (without implantation) is quite close to the theoretically predicted value of a pseudomorphically

grown  $\text{Si}_{1-x}\text{Ge}_x/\text{Si}$  heterojunction, whilst for samples  $S_2$  and  $S_3$  (without implantation) the experimental  $\Delta E_v$  deviates from the theoretically predicted one which resulted from strain relaxation. Since the strains in samples  $S_2$  and  $S_3$  are partially or fully relaxed, the low-dose boron-ion implantation does not induce an  $\text{H}_2$  defect. Instead, another minority carrier peak  $E_2$  appears in figure 3 for sample  $S_3$ . Also, the significant reduction and disappearance of the  $\text{H}_2$  peak in samples  $S_2$  and  $S_3$  compared with sample  $S_1$  illustrate that the defect  $\text{H}_2$  induced by ion implantation is related to the built-in strain in the quantum well.

To remove the ion-implantation-induced defects, rapid thermal annealing (RTA) was performed on sample  $S_1$  at the temperatures of 400, 600, 800 and 1000 °C for 10 s in nitrogen ambient. The DLTS spectra after RTA are shown in figure 6. The defect peak  $\text{H}_2$  is greatly suppressed after RTA at 600 °C and vanishes after RTA at 1000 °C. However, the high-temperature treatment causes strain relaxation and interfacial atomic intermixing in the quantum well. In this work we measured the DLTS of sample  $S_1$  (without ion implantation) after RTA treatments and determined the valence band offsets from the well emission peak  $\text{H}_1$ . The results show that  $\Delta E_v$  varies with the annealing temperature: 0.24 eV for 400 °C, 0.22 eV for 600 °C and 0.21 eV for 800 °C. After RTA at 1000 °C, peak  $\text{H}_1$  is no longer visible so that the band offset cannot be determined from DLTS. The change in  $\Delta E_v$  after RTA is indicative of the strain relaxation. This result is the same as that of [11]. Our admittance spectroscopic measurements showed that the valence band offset of sample  $S_1$  after implantation remains unchanged if the RTA temperature is no higher than 600 °C. For the quantum well sample processed with ion implantation it seems that the annealing temperature of 600 °C is appropriate not only for removing most of the defects induced by the implantation but also to avoid strain relaxation.



**Figure 6.** DLTS spectra of sample  $S_1$  after RTA at different temperatures. (○) no annealing, (a) 400 °C, (b) 600 °C, (c) 800 °C and (d) 1000 °C.

In conclusion, by using DLTS measurements, it has been found that low-dose boron-ion implantation in the cap region of the  $\text{Si}_{1-x}\text{Ge}_x/\text{Si}$  single quantum well will induce an acceptor-like defect level in the well region at an energy of 0.52 eV above the silicon valence band edge. This defect level is generated by the interaction between the implantation-induced lattice distortion and the built-in strain, so its concentration is closely related to the strain. For the sample in which the strain is relaxed, the acceptor-like defect is not visible in the DLTS spectrum, instead an electron trap level is formed as a result of ion

implantation. After treating the sample by RTA at 600 °C, most of the ion-implantation-induced defects could be removed while the interfaces of the quantum well still keep the pseudomorphic form. To remove the defects induced by ion implantation fully, the RTA should be performed at 1000 °C but the strain in the quantum well will be relaxed by such a high-temperature treatment.

### Acknowledgments

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